

SSF-I-1541

15 Nov 65

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STATINTL

AKP-I-4903

9 November 1965

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TO:

REF:

- (A) ESP GR-22 Retention of BIT Information and Letter AKP-I-4443 of June 21, 1965
- (B) Flight Test Report RSF-I-142 of November 4, 1965 Flight 3-15-24 on October 22, 1965
- (C) AKP-I-4736 of September 29, 1965

Dear Phil:

Submitted for your approval is GR-32. Flight Test Report RSF-I-142 (Ref. B), results have confirmed the satisfactory operation of the retention of "GO" indication in airborne BITs with the new Diode Board circuit in use. RSO comments about this change are that results are very good and it is a system improvement.

The new drawings required for this change will be as follows:

Diode Board Ass'y	P/N
Diode Board	531A600-009
Diode Board Schematic	531A600-008
Spacer	531A600-101
Insulated Spacer	P/N to be assigned
	P/N to be assigned

The following drawings will be revised:

SLR Test Unit Ass'y	531A600-001
Harness Ass'y Branched	531A600-005
SLR Test Unit Schematic	531A600-100

Retrofit cost for the eight (8) prototype units will be \$8,660.23.

LP-2263

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encl.
cc: Bob D.

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